

ABSTRACT OF THE DISCLOSURE

An electro-optical device includes pixel electrodes formed on a TFT array substrate and a counter electrode formed on a counter substrate. A data line is buried in a groove formed in the TFT array substrate for planarization. A projection and a hollow portion are formed by burying a capacitance line, rather than a scanning line, into the groove. A downwardly rubbed portion, which is downwardly rubbed, of inclined surfaces of the projection and the hollow portion, is covered with a light-shielding layer, and upwardly rubbed portions are covered with no light-shielding layer. With this arrangement, the electro-optical device provides a high pixel aperture ratio and presents a high-contrast and bright image by reducing an orientation defect of a liquid crystal resulting from a step portion in the surface of the substrate facing the liquid crystal.

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